MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
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NBA Accredited - B.Tech. (CIVIL, CSE, ECE, EEE, MECH, CST), MBA & MCA

A Report on One Day Seminar on "Advanced Technologies and Trends in VLSI Testing and Physical Design" Organised by Department of Electronics & Communication Engineering in association with ISTE Student Chapter on 17.02.2026

Report submitted by: Mr. G. Charan Kumar, Assistant Professor, Department of ECE.
Resource Person Details: Dr. Ashraf Ali Shaik, Founder and CEO of Siliquan Technologies Pvt. Ltd.,
Report Received on 02.03.2026.
Total No. of Registrations: 350
Mode of Conduct: Offline

About the Workshop:
The **Department of Electronics and Communication Engineering (ECE)**, in association with the **ISTE Student Chapter**, successfully organized a **One Day Seminar on "Advanced Technologies and Trends in VLSI Testing and Physical Design"** on **17th February 2026** at **Auditorium** from **10.00 AM to 12.30 PM**.
The primary objective of the seminar was to provide students and faculty members with insights into the latest advancements, industry practices, and emerging trends in the field of **VLSI Testing and Physical Design**, bridging the gap between academic learning and industrial requirements.


The program commenced with a **Welcome address by Dr. S. Rajasekaran, Professor and Head, Department of ECE**, who warmly welcomed the resource person, faculty members, and students. In his address, he emphasized the growing importance of VLSI technologies in semiconductor industries and encouraged students to gain practical knowledge aligned with industry expectations.

The seminar was delivered by **Dr. Ashraf Ali Shaik**, **Founder and CEO of Siliquan Technologies Pvt. Ltd.**, an expert in VLSI design and semiconductor technology. The resource person shared valuable knowledge on advanced methodologies used in modern chip design, testing techniques, and current industrial trends. He explained key concepts such as physical design flow, design verification, timing analysis, design for testability (DFT), and challenges faced in nanoscale semiconductor fabrication.

Dr. Ashraf Ali Shaik also highlighted career opportunities available in the VLSI domain and guided students on the required technical skills, tools, and industry certifications needed to build successful careers in semiconductor companies. The session included practical industry examples, making the seminar highly interactive and informative.

The event was coordinated by **Dr. G. Naga Jyothi, Associate Professor, Department of ECE**, along with faculty members and a large number of ECE students who actively participated in the session. Students engaged enthusiastically during the interaction session, clarifying their doubts related to VLSI career paths and industry expectations.
The seminar concluded with an interactive discussion and a vote of thanks, expressing gratitude to the resource person for sharing his expertise and to the management for supporting academic enrichment activities.

Program Outcomes

After attending the seminar, participants were able to:

1. Understand advanced concepts and emerging trends in VLSI Testing and Physical Design.
2. Gain knowledge about industry-oriented design flow and testing methodologies.
3. Learn about modern semiconductor challenges and technological advancements.
4. Identify career opportunities and required skill sets in the VLSI industry.
5. Enhance awareness of practical applications of VLSI technologies in real-world scenarios.
6. Develop motivation to pursue higher studies and careers in semiconductor and chip design domains.

Vote of Thanks

On behalf of the Department of Electronics and Communication Engineering, MITS Deemed to be University, it is my great honor to propose the Vote of Thanks for today's seminar on **"Advanced Technologies and Trends in VLSI Testing and Physical Design."**

First and foremost, I would like to express our heartfelt gratitude to our esteemed resource person, **Dr. Ashraf Ali Shaik**, Founder and CEO of Siliquan Technologies Pvt. Ltd., for delivering such an insightful and knowledge-enriching session. Your expertise, industry experience, and valuable guidance have provided our students and faculty with a deeper understanding of current trends, challenges, and opportunities in the VLSI domain. We truly appreciate the time and effort you have taken to share your knowledge with us.I extend my sincere thanks to the Management of MITS Deemed to be University for their constant encouragement and support in organizing academic programs that enhance technical learning and professional development. I would like to convey my respectful gratitude to our Honourable **Vice Chancellor (i/c), Dr. C. Yuvaraj Sir**, for his continuous motivation and visionary leadership that promotes academic excellence and innovation.
My heartfelt thanks to our respected **Principal, Dr. Ramanathan Sir**, for his valuable support and guidance in facilitating this seminar successfully. I would also like to express my sincere appreciation to **Dr. S. Rajasekaran Sir, Professor and Head of the Department of ECE**, for his constant encouragement, leadership, and support in organizing such meaningful academic initiatives for the benefit of students. I extend my special thanks to **Dr. Naga Jyothi Madam, Associate Professor, Department of ECE**, and all the faculty members for their efforts in coordinating and supporting this program.
I also thank all the III Year Students and Student Coordinators of the event for their enthusiastic participation and active involvement, which made this seminar interactive and successful.

Newspaper clips:


సూర్య

విఎల్‌ఎసిఐ చిప్ తయారీ రంగంలో పుష్కలమైన ఉద్యోగావకాశాలు

- విద్యా యూనివర్సిటీ విద్యార్థులకు వెబినార్

ఆంధ్రప్రభ

మిట్స్ యూనివర్సిటీలో ఆధునిక సాంకేతికతపై సదస్సు

కురబలకోట, ఫిబ్రవరి 25 (ఆంధ్రప్రభ):

- మిట్స్ యూనివర్శిటీ విద్యార్థులకు సెమినార్

వి.ఎల్.ఎస్.ఐ చిప్ తయారీ రంగంలో పుష్కలమైన ఉద్యోగావకాశాలు